WEST Search History

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DATE: Thursday, August 26, 2004

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DB=PGPB, $USPT$, $JPAB$; $PLUR=YES$; $OP=OR$			
	L21	L20 and noise and gate	5
(ne	L20	4806801	14
	L19	L18 and static	28
	L18	L17 and (gate same width)	115
	L17	gate same noise same (drc or check\$4 or (design same rule))	414
]	L16	114 and L15	40
	L15	noise same (drc or checker or (design same rule))	703
	L14	gate same width same noise	2220
	L13	16 and L12	12
	L12	noise same figure	14642
	L11	L10 and width and (mos or cmos or fet)	18
	L10	gate same noise same (drc or (design same rule))	48
	L9	L8 and (noise same (immun\$ or suscept\$))	14
1000	L8	L7 and width	47
	L7	13 and L5	47
T _{reper} j	L6	12 and L5	164
	L5	gate same noise same (mos or cmos or fet)	4813
	L4	L3 and (noise same (suscept\$ or immun\$))	38
Ţ.	L3	L2 and width and (pmos and nmos)	94
	L2	L1 and (drc or (design same rule))	402
	L1	noise same (mos or cmos or fet)	14348

END OF SEARCH HISTORY

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